

GARIMA SINGH

Email: garimasingh.ece@gmail.com

Mob: 7987596359

Profile: <https://www.linkedin.com/in/garima-singh-362a3058/>

Location: Mumbai, Maharashtra

Career Objective:

As an experienced faculty in the field of Electronics and Telecommunication Engineering with MTech in VLSI Design, seeking a position as an Assistant Professor, where I can share my knowledge and industrial experience with students for their continuous growth.

Education:

Examination	University/Board	Time Duration	Percentage/CGPA
PhD (Electronics) (Pursuing)	Fr. Conceicao Rodrigues College of Engineering, Mumbai University	2025-Present	-
M.Tech (VLSI Design)	Banasthali Vidyapith	2015-2017	76.13%
B.E. (Electronics and Communication Engineering)	Rajiv Gandhi Proudhyogiki Vishwavidyalaya (RGPV)	2010-2014	8.1
HSC	St. Joseph's Convent School (CBSE)	2010	68.6 %
SSC	St. Joseph's Convent School (CBSE)	2008	78 %

Areas of Interest:

Electronic Devices, Artificial Intelligence and Machine Learning, Communication System, Basic Electrical Engineering, Analog Circuits, VLSI Design, Internet of Things, Image Processing, Digital Electronics

Teaching Experience:

(6 years and 4 months total)

1. College: Shri Ram Institute of Science and Technology (SRIST), Jabalpur

- **Duration:** July 2018 to March 2022 (3 years and 8 months)
- **Designation:** Assistant Professor
- **Department:** Electronics and Communication Engineering (ECE) Dept., B.E.
- **Subjects Taught:** Electronic Devices, Analog Circuits, Communication System, Signals and Systems
- **Other Responsibilities:** Workshop and Seminar co-ordinator, Class in-charge, Organizer in Cultural and Technical events

2. College: St. Francis Institute of Technology, Borivali

- **Duration:** July 2022 to June 2024 (2 Years)
- **Designation:** Assistant Professor (Ad-hoc)
- **Department:** Information Technology (INFT) Dept., B.E.
- **Subjects Taught:** Principle of Communication, Image Processing, Internet of Everything (IoE), Basics of Electrical Engineering (BEE), Computer Programming
- **Other Responsibilities:** Basics of Electrical Engineering (BEE) Lab, Computer Programming Lab, Internet of Everything (IoE) lab, Sensor Lab, Computer Networking Lab (S.E) Guiding B.E. final project, T.E. mini projects, member of organising committee of FDP and other administrative works

3. College: Fr. Conceicao Rodrigues College of Engineering, Bandra

- **Duration:** From July 2024 to present (8-Month)
- **Designation:** Assistant Professor (Ad-hoc)
- **Department:** Electronics and Computer Science Department
- **Subjects taught:** Basic Electrical and Electronics Engineering (BEEE), Machine Learning, Artificial Intelligence
- **Other Responsibilities:** BEEE lab, Digital Electronics lab, Artificial Intelligence lab, Essential Psychomotor Skills for Engineers lab and other administrative works

Skill sets:

Programming Language	C, Verilog HDL
EDA Tools	Cadence Virtuoso, Tanner, Xilinx ISE, Multisim, Active HDL
Softwares	MS office, MATLAB, CodeBlocks, NS2, Cisco Packet Tracer, Python

Certification Courses:

1. Faculty Induction Programme (FIP) organized by Basic Science Basic Science & Humanities Dept, St Francis Institute of Technology (SFIT), Borivali with IQAC, 2022
2. Artificial Intelligence & Data Science organized by IDEMI, Government of India in May 2022
3. STTP on “Building Solutions Using No Code AI and Machine Learning” organized by ISTE, TCET in January 2023
4. FDP on “Data Science and Chat GPT” organized by K L & ExcelR from 15/05/2023 to 19/05/2023
5. Faculty Development Programme (FDP) course by NPTEL-AICTE on “Introduction to Internet of Things” (Elite Silver), Jul-Oct 2023
6. ISTE approved FDP on “Cultivating the Future of Healthcare with Generative AI” organized by St. Francis Institute of Technology, Mumbai, Maharashtra from 11/12/2023 to 15/12/2023

7. ISTE approved FDP on “Generative Adversarial Network and Auto Encoders for Computer Vision” organized by Thadomal Shahani Engineering College (TSEC), Mumbai, Maharashtra from 03/06/2024 to 08/06/2024
8. IEEE approved FDP on “Becoming IoE ready: Recent Trends in Sensing, Security and Data Analysis” organized by St. Francis Institute of Technology, Borivali and Thakur College of Engineering & Technology from 24/06/2024 to 29/06/2024
9. Faculty Development Programme (FDP) course by NPTEL-AICTE on “Introduction to Machine Learning” (Elite Silver), Jul-Sept 2024

Academic Projects:

1. Layout design and Post Layout Simulation of Analog Memory ASIC blocks (MTech Project)

2.5 GHz DLL based analog memory ASIC named “ANUSMRITI” is designed in 180 nm mixed-signal CMOS process at Electronics Division (ED), Bhabha Atomic Research Centre (BARC).

Duration: July 2016- April 2017 (10 months)

Designation: Project Trainee

2. Face Recognition Security System Using MATLAB (BE Final Year Project)

The face recognition system is developed based on Local Binary Pattern (LBP) method using MATLAB software.

Publications:

- A. Dhamelia, G. Harpanhalli, A. Doshi, A. Kabsuri, M. Lopes and G. Singh, "IoE-Based Predictive Oxygen Inventory Management," 2023 International Conference on Communication System, Computing and IT Applications (CSCITA), Mumbai, India, 2023, pp. 219-224, doi: 10.1109/CSCITA55725.2023.10104693.
- B. Dr. Aruna Pavate, Ms. Akanksha Naik, Ms. Vijayalakshmi Balaraju, Ms. Garima Singh, Ms. Priyanka Patil, Ms. Aaysha Shaikh, “IOT BASED DIGITAL HYDROPONIC NUTRIENT PREDICTION DEVICE IN A HYDROPONIC SYSTEM”, Intellectual Property India, Patent Application No. 202321039420, published date: 18/08/2023

Date: 24/03/2025

GARIMA SINGH