

## FACULTY PROFILE



NAME :Ms. Archana Pascal Lopes  
DESIGNATION :Assistant Professor  
QUALIFICATION :ME (Electronics)  
EMAIL :archana\_lopes@frcrce.ac.in  
AREAS OF INTEREST: Object Oriented Programming Methodology ,  
Neural Network and Fuzzy Logic, Computer  
Network, Advanced Computer Networks

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### PROFESSIONAL BACKGROUND

#### **Teaching Experience:**

March 2013 –till date	Fr.CRCE, Mumbai	Assistant professor
August 2009 – 2013	Fr.CRCE, Mumbai	Lecturer
July 2007 – July 2009	Vidyavardhini's A.V. College, Vasai	Lecturer(B.Sc(IT))
July 2005-March 2007	St. Gonsalo Garcia, Vasai	Lecturer (B.Sc(IT))

#### **Industry Experience:**

October 2003-September 2004	NucSoft Ltd., Mumbai	System Engineer
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#### **EDUCATIONAL DETAILS**

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<b>BE in Electronics</b>	Mumbai University	2002
<b>ME in Electronics</b>	Mumbai University	2009

#### **ADMINISTRATIVE BACKGROUND**

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- Time table co-ordinator
- Alumni Committee Member

Ph.D. SUPERVISED : Nil

**PUBLICATIONS:**

- Archana Lopes, Sangeeta Parshionikar, Heenakausar Pendhari, " Early detection of Heart Diseases using BRANN", IJCRT, (ISSN : 2320-2882/Volume 6 | Issue 2 | APRIL 2018, ISSN- 2320-2882),Pg. No. 741-745
- Sangeeta Parshionikar, Archana Lopes, Heenakausar Pendhari, " Generating Hardware Fingerprint of FPGA using Physical Unclonable Function", International Conference on Recent Innovations in Electrical, Electronics & Communication Engineering - (ICRIEECE)-2018 Bhuvaneshwar, (ISSN 978-1-5386-5994-6)

**BOOKS AUTHORED** : Nil

**TRAINING PROGRAMS CONDUCTED** :

1. Co-ordinated two day faculty development program on "Python Programming :Basics to Advanced. " organized by department of Electronics, CRCE on 30 and 31st August 2019
2. Co-ordinated two day faculty development program on "Hands on FPGA Applications " organized by department of Electronics, CRCE on 23rd and 24th February 2018
3. Co-ordinated two day faculty development program on "Advances in Communication Networks " organized by department of Electronics, CRCE
4. Co-ordinated two day faculty development program on "Techniques of Technical Paper Writing" organized by department of Electronics
5. Co-ordinated two day faculty development program on "Industrial Automation" organized by department of Electronics, CRCE on 11<sup>th</sup> – 12<sup>th</sup> March 2010.

**PARTICIPATION IN TRAINING PROGRAM** :

1. 12 weeks NPTEL online course on "Problem Solving Through Programming in C", December 2018
2. Two Days AICTE Sponsored National Seminar on " Technologies for Development of Rural Areas", in SPIT, Mumbai
3. Three Days FDP on " Wireless Network Simulation using NS2 and Matlab- Hands on Training", in Xavier's Institute of Technology, Mumbai
4. Three day FDP on "ANN-Fuzzy Logic", in Xavier's Institute of Technology, Mumbai
5. Eight week Training Program on "Embedded Systems" ThinkLabs,2011.
6. Two week graded STTP on "Communication Networks: Practical Approach" conducted by SPIT, Bombay,December 2010.
7. One week STTP on "Image Processing Applications :Basics to Advanced" atSt. Francis Institute of Technology in January 2010.

**PROFESSIONAL AFFILIATIONS** : ISTE Life Member